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U.S. **UTILITY** Patent Application

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APPLICATION NO. 09/660484	CONT/PRIOR F	CLASS 438	SUBCLASS 10	ART UNIT 2812	EXAMINER <i>Shirley A. [Signature]</i>
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ITS Yasuo Tanaka

Method of manufacturing semiconductor device

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____  _____	_____  _____ (Primary Examiner)		<b>ISSUE FEE</b>  <table border="1" style="width: 100%;"> <tr> <td style="width: 50%; text-align: center;">Amount Due</td> <td style="width: 50%; text-align: center;">Date Paid</td> </tr> </table>		Amount Due	Date Paid
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